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Extended OS

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Learning Outcomes

- An appreciation that the abstract interface to the system can be at different levels.
 - Virtual machine monitors (VMMs) provide a low-level interface
- An understanding of trap and emulate
- Knowledge of the difference between type 1 and type 2 VMMs
- An appreciation of some of the issues in virtualising the R3000

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Virtual Machines

References:
 Smith, J.E.; Ravi Nair; , "The architecture of virtual machines,"
Computer , vol.38, no.5, pp. 32- 38, May 2005
 Chapter 8.3 Textbook "Modern Operating Systems"

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Abstraction & Virtualisation

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Interface Levels

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Instruction Set Architecture

- Interface between software and hardware
- Divided between privileged and un-privileged parts
 - Privileged a superset of the un-privileged

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Application Binary Interface

- Interface between programs ↔ hardware + OS
- Consists of system call interface + un-privileged ISA

The diagram shows a layered architecture. At the top is 'Software' containing 'Application programs' and 'Libraries'. Below this is the 'Operating system'. The 'Execution hardware' layer contains 'System interconnect (bus)' and 'Memory translation'. At the bottom is 'Hardware' containing 'I/O devices and networking' and 'Main memory'. Arrows indicate the flow of data and control between these components.

Application Programming Interface

- Interface between high-level language ↔ libraries + hardware + OS
- Consists of library calls + un-privileged ISA
 - Syscalls usually called through library.
- Portable via re-compilation to other systems supporting API

The diagram is identical to the one for the Application Binary Interface, showing the same layered architecture from software to hardware.

Process versus System Virtual Machine

(a) Process virtual machine: A guest application process runs on top of virtualizing software and an OS, which in turn runs on the host hardware.

(b) System virtual machine: A guest with applications and an OS runs on a virtual machine monitor (VMM) that virtualizes the hardware. The VMM runs on the host hardware.

OS is an extended virtual machine

- Multiplexes the "machine" between applications
 - Time sharing, multitasking, batching
- Provides a higher-level machine for
 - Ease of use
 - Portability
 - Efficiency
 - Security
 - Etc....

JAVA – Higher-level Virtual Machine

- write a program once, and run it anywhere
 - Architecture independent
 - Operating System independent
- Language itself was clean, robust, garbage collection
- Program compiled into bytecode
 - Interpreted or just-in-time compiled.
 - Lower than native performance

Conventional versus Emulation/Translation

(a) Conventional compilation: HLL program is processed by a compiler front end to produce intermediate code, then a compiler back end to produce object code, which is loaded into a memory image.

(b) Translation: HLL program is processed by a compiler to produce portable code, which is loaded by a VM loader into a virtual memory image, then executed by a VM interpreter/compiler to produce host instructions.

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Aside: Just In-Time compilation (JIT)

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Issues

- Legacy applications
- No isolation nor resource management between applets
- Security
 - Trust JVM implementation? Trust underlying OS?
- Performance compared to native?

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Is the OS the “right” level of extended machine?

- Security
 - Trust the underlying OS?
- Legacy application and OSs
- Resource management of existing systems suitable for all applications?
- What about activities requiring “root” privileges

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Virtual Machine Monitors

- Provide scheduling and resource management
- Extended “machine” is the actual machine interface.

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IBM VM/370

- CMS a light-weight, single-user OS
- VM/370 multiplex multiple copies of CMS

The diagram illustrates the architecture of IBM VM/370. At the top, 'Virtual 370s' are shown as three separate boxes, each containing a 'CMS' (Control Macro System) instance. These are managed by the 'VM/370' layer. Below this is the '370 Bare hardware'. Arrows indicate the flow of control and data: 'I/O instructions here' enter from the left into the CMS instances; 'System calls here' enter from the right into the CMS instances; and 'Trap here' enters from the left into the VM/370 layer.

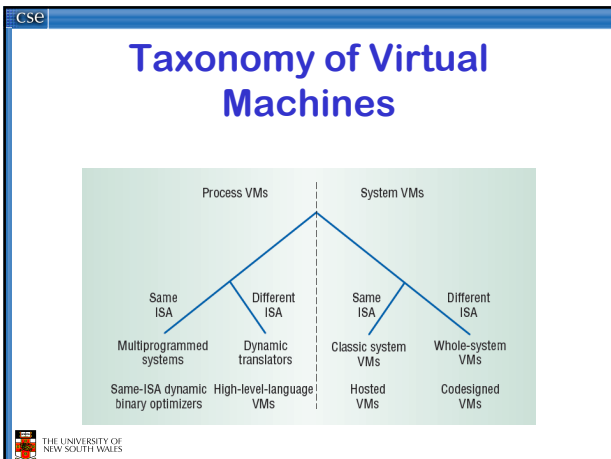
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Advantages

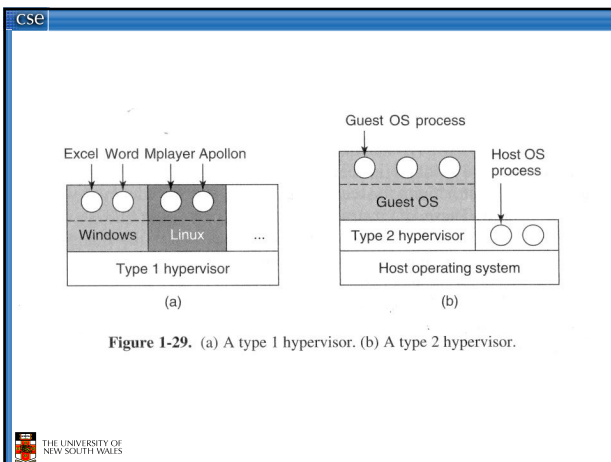
- Legacy OSES (and applications)
- Server consolidation
- Concurrent OSES
 - Linux – Windows
 - Primary – Backup
 - High availability
- Test and Development
- Security
 - VMM (hopefully) small and correct
- Performance near bare hardware
 - For some applications

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What is System/161?

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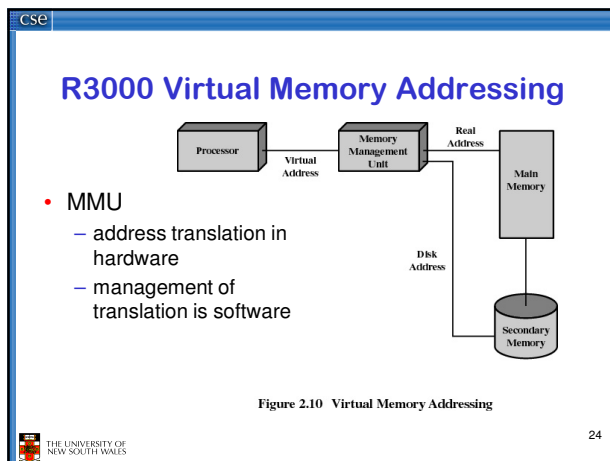


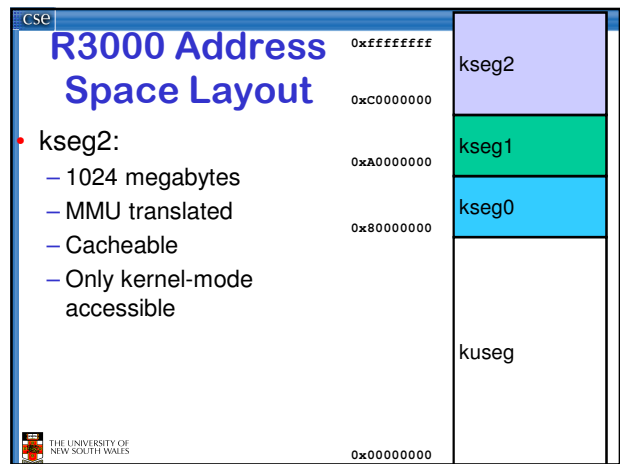
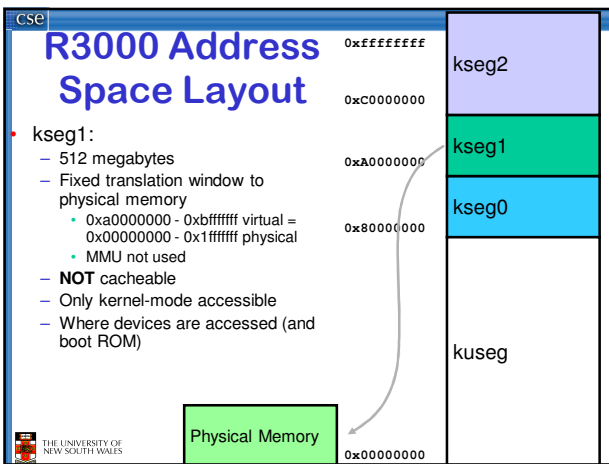
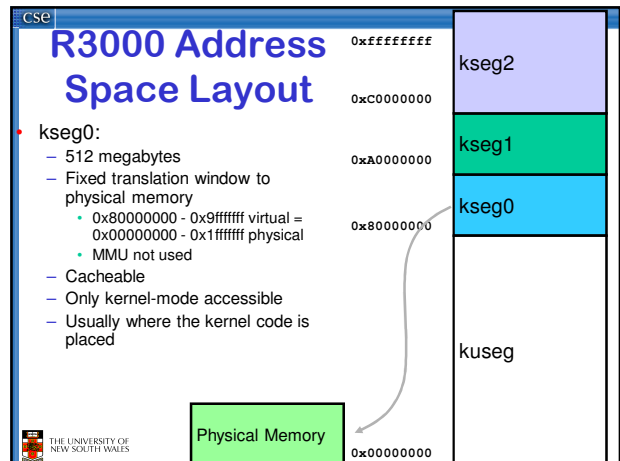
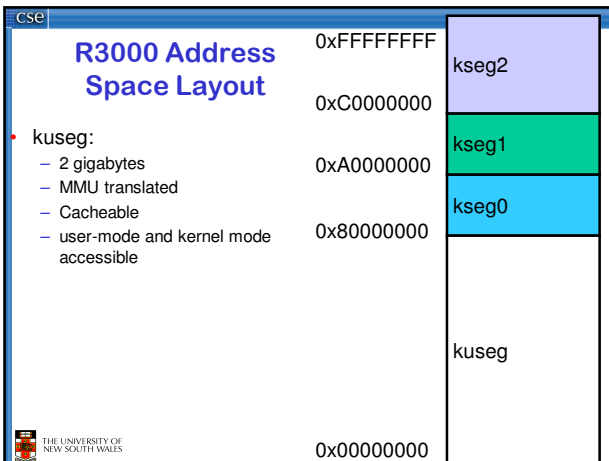
- ## Virtual R3000???
- Interpret
 - System/161
 - slow
 - JIT dynamic compilation
 - Run on the real hardware??
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Gerald J. Popek and Robert P. Goldberg (1974). "Formal Requirements for Virtualizable Third Generation Architectures". Communications of the ACM 17 (7): 412–421.

- Sensitive Instructions
 - The instructions that attempt to change the configuration of the processor.
 - The instructions whose behaviour or result depends on the configuration of the processor.
- Privileged Instructions
 - Instructions that trap if the processor is in user mode and do not trap if it is in system mode.
- Theorem
 - Architecture is virtualisable if sensitive instructions are a subset of privileged instructions.

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- ### Issues
- Privileged registers (CP0)
 - Privileged instructions
 - Address Spaces
 - Exceptions (including syscalls, interrupts)
 - Devices

Approach: Trap & Emulate?

