

COMP3161/COMP9161

Syntax Exercises

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1. Here is a concrete syntax for specifying binary logic gates with convenient **if – then – else** syntax. Note that the **else** clause is optional, which means we must be careful to avoid ambiguity – we introduce mandatory parentheses around nested conditionals:

$$\begin{array}{c}
 \frac{\frac{\frac{\frac{\top \text{ OUTPUT}}{c \text{ INPUT}} \quad \frac{\perp \text{ OUTPUT}}{t \text{ IEXPR}} \quad \frac{\alpha \text{ INPUT}}{e \text{ EXPR}} \quad \frac{\beta \text{ INPUT}}{x \text{ OUTPUT}}}{\text{if } c \text{ then } t \text{ else } e \text{ EXPR}} \quad \frac{\frac{e \text{ EXPR}}{e \text{ IEXPR}} \quad \frac{e \text{ IEXPR}}{e \text{ EXPR}}}{(e) \text{ IEXPR}}}{\text{if } c \text{ then } t \text{ EXPR}} \quad \frac{x \text{ OUTPUT}}{x \text{ IEXPR}}
 \end{array}$$

If an **else** clause is omitted, the result of the expression if the condition is false is defaulted to \perp . For example, an AND or OR gate could be specified like so:

AND : if α then (if β then \top)

OR : if α then \top else (if β then \top)

Or, a NAND gate:

if α then (if β then \perp else \top) else \top

- (a) [★★] Devise a suitable *abstract syntax* A for this language.

Solution:

$$\frac{x \in \{a, b\}}{x \text{ INPUT}} \quad \frac{x \in \{\top, \text{F}\}}{x \text{ OUTPUT}} \quad \frac{c \text{ INPUT} \quad t \text{ A} \quad e \text{ A} \quad x \text{ OUTPUT}}{\text{If } c \text{ t } e \text{ A}} \quad \frac{x \text{ OUTPUT}}{x \text{ A}}$$

- (b) [★] Write rules for a *parsing relation* (\longleftrightarrow) for this language.

Solution:

$$\begin{array}{c}
 \frac{}{\top \text{ OUTPUT} \longleftrightarrow \top}^{\text{TOP}} \quad \frac{}{\perp \text{ OUTPUT} \longleftrightarrow \text{F}}^{\text{BOT}} \quad \frac{}{\alpha \text{ INPUT} \longleftrightarrow \text{A}}^{\text{INPUT}_\alpha} \quad \frac{}{\beta \text{ INPUT} \longleftrightarrow \text{B}}^{\text{INPUT}_\beta} \\
 \frac{\frac{c \text{ INPUT} \longleftrightarrow c' \quad t \text{ IEXPR} \longleftrightarrow t' \quad e \text{ EXPR} \longleftrightarrow e'}{\text{if } c \text{ then } t \text{ else } e \text{ EXPR} \longleftrightarrow \text{If } c' \text{ t' } e'}^{\text{IF}_1} \quad \frac{\frac{c \text{ INPUT} \longleftrightarrow c' \quad t \text{ IEXPR} \longleftrightarrow t'}{\text{if } c \text{ then } t \text{ EXPR} \longleftrightarrow \text{If } c' \text{ t' F}}^{\text{IF}_2}}{\frac{e \text{ EXPR} \longleftrightarrow e'}{(e) \text{ IEXPR} \longleftrightarrow e'}^{\text{PAREN}} \quad \frac{e \text{ OUTPUT} \longleftrightarrow e'}{e \text{ IEXPR} \longleftrightarrow e'}^{\text{SHUNT}_1} \quad \frac{e \text{ IEXPR} \longleftrightarrow e'}{e \text{ EXPR} \longleftrightarrow e'}^{\text{SHUNT}_2}}
 \end{array}$$

- (c) [★] Here's the parse derivation tree for the NAND gate above:

$$\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\perp \text{ OUTPUT} \longleftrightarrow \top}{\top \text{ IEXPR} \longleftrightarrow \top}}{\top \text{ EXPR} \longleftrightarrow \top}}{\text{if } \beta \text{ then } \perp \text{ else } \top \text{ EXPR} \longleftrightarrow \top}}{\beta \text{ INPUT} \longleftrightarrow \perp} \quad \frac{\frac{\frac{\frac{\frac{\frac{\perp \text{ OUTPUT} \longleftrightarrow \top}{\top \text{ IEXPR} \longleftrightarrow \top}}{\top \text{ EXPR} \longleftrightarrow \top}}{\text{if } \beta \text{ then } \perp \text{ else } \top \text{ EXPR} \longleftrightarrow \top}}{\perp \text{ OUTPUT} \longleftrightarrow \top}}{\perp \text{ IEXPR} \longleftrightarrow \top}}{\perp \text{ EXPR} \longleftrightarrow \top}}}{\alpha \text{ INPUT} \longleftrightarrow \perp} \quad \frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\perp \text{ OUTPUT} \longleftrightarrow \top}{\top \text{ IEXPR} \longleftrightarrow \top}}{\top \text{ EXPR} \longleftrightarrow \top}}{\text{if } \beta \text{ then } \perp \text{ else } \top \text{ EXPR} \longleftrightarrow \top}}{\perp \text{ OUTPUT} \longleftrightarrow \top}}{\perp \text{ IEXPR} \longleftrightarrow \top}}{\perp \text{ EXPR} \longleftrightarrow \top}}}{\text{if } \beta \text{ then } \perp \text{ else } \top \text{ EXPR} \longleftrightarrow \top}}}{\text{if } \alpha \text{ then (if } \beta \text{ then } \perp \text{ else } \top) \text{ EXPR} \longleftrightarrow \top}}$$

implement our interpreter, rather than the language being implemented). This function is (extensionally) equal to any other α -equivalent function, and therefore we can consider two α -equivalent terms to be equal with HOAS, assuming extensionality (that is, a function f equals a function g if and only if, for all x , $f(x) = g(x)$).

For example, a first order Haskell implementation of the above syntax might look like this:

```
type VarName = String
data AST = App AST AST
         | Var VarName
         | Lambda VarName AST
test = Lambda "x" (Lambda "y" (App (Var "x") (Var "y")))
```

Whereas a higher order syntax might look like this:

```
data AST = App AST AST
         | Lambda (AST -> AST)
test = Lambda $ \x -> Lambda $ \y -> App x y
```

There is no way in Haskell, for example, to determine that we used the names `x` and `y` for those function arguments. The only way for a Haskell function `f` to be distinguished from a function `g` is for `f x` to be different from `g x` for some x (i.e extensionality). As α -equivalent Haskell functions cannot be so distinguished, we must judge a term as equal to any other in its α -equivalence class.