

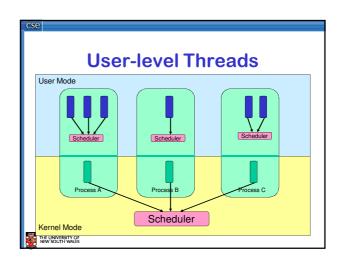
Learning Outcomes

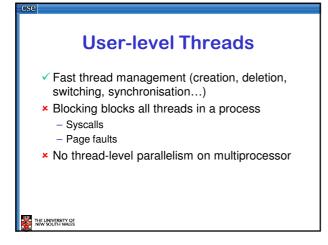
- An understanding of hybrid approaches to thread implementation
- A high-level understanding of scheduler activations, and how they overcome the limitations of user-level and kernel-level threads.

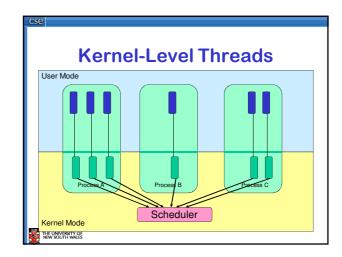
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 Thomas Anderson, Brian Bershad, Edward Lazowska, and Henry Levy. Scheduler Activations: Effective Kernel Support for the User-Level management of Parallelism. ACM Trans. on Computer Systems 10(1), Feburary 1992, pp. 53-79.

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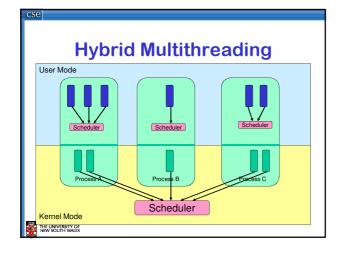
Kernel-level Threads

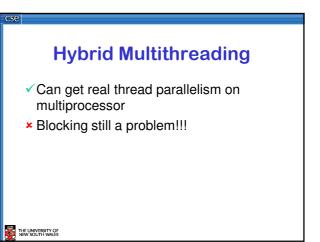
- Slow thread management (creation, deletion, switching, synchronisation...)
 - System calls
- Blocking blocks only the appropriate thread in a process
- Thread-level parallelism on multiprocessor

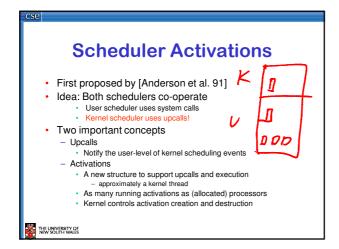
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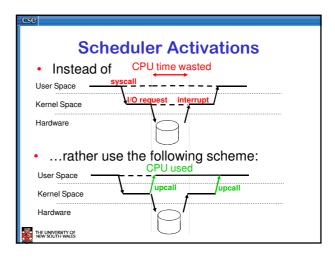
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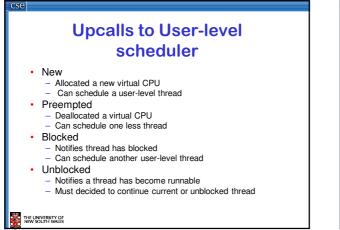
Operation	FastThreads	Topaz threads	Ultrix processe
Null Fork	34	948	11300
Signal-Wait	37	441	1840

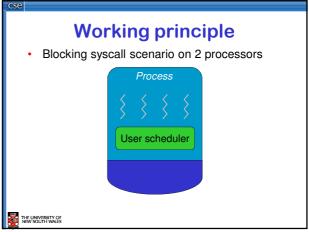


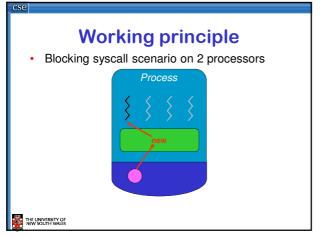


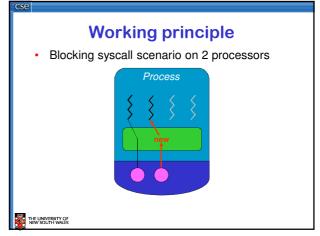


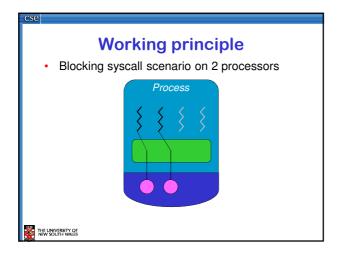


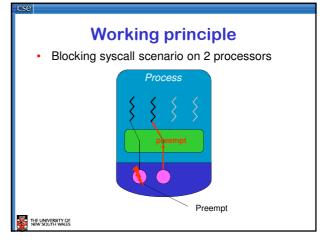


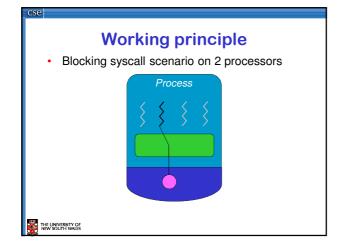


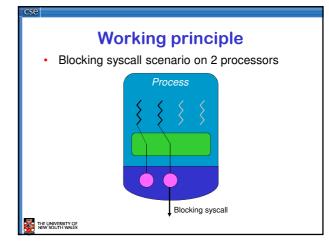


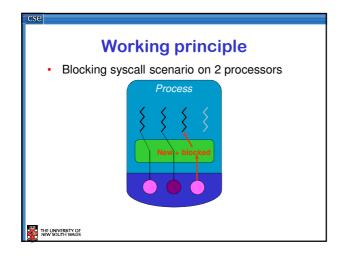


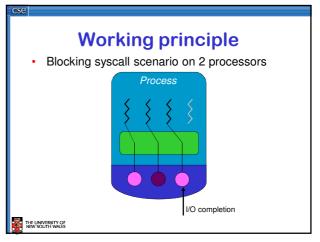


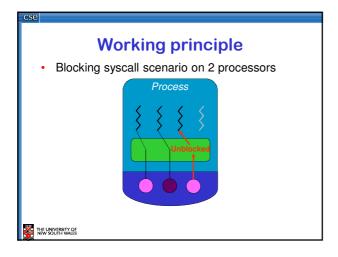


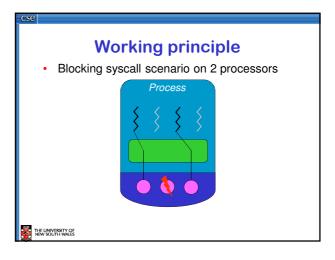












Scheduler Activations

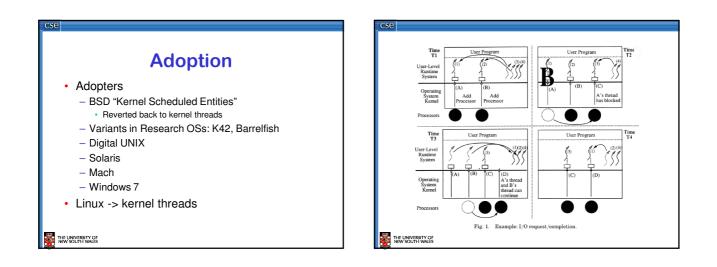
- Thread management at user-level
 Fast
- Real thread parallelism via activations

 Number of activations (virtual CPU) can equal CPUs
- Blocking (syscall or page fault) creates new activation
 - User-level scheduler can pick new runnable thread.
- Fewer stacks in kernel
 - Blocked activations + number of virtual CPUs

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Table IV. Thread Operation Latencies (µsec.)						
Operation	FastThreads on Topaz Threads	FastThreads on Scheduler Activations	Topaz threads	Ultrix process		
Null Fork	34	37	948	11300		
Signal-Wait	37	42	441	1840		



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