

# COMP 3152 Homework 11

This homework centres around Peterson’s famous mutual exclusion algorithm as running example. It is an improvement of the brilliant original algorithm of Dekker.

The algorithm deals with two concurrent processes A and B that want to alternate critical and noncritical sections. Each of these processes will stay only a finite amount of time in its critical section, although it is allowed to stay forever in its noncritical section. The purpose of the algorithm is to ensure that they are never simultaneously in the critical section, and to guarantee that both processes keep making progress.

The processes use three variables. The boolean variable *readyA* can be written by process A and read by process B, whereas *readyB* can be written by B and read by A. By setting *readyA* to *true*, process A signals to process B that it wants to enter the critical section. The variable *turn* is a shared variable: it can be written and read by both processes. Its use is the brilliant part of the algorithm. Initially *readyA* and *readyB* are both *false* and *turn* = A.

## Process A

repeat forever

$$\left( \begin{array}{l} \ell_1 \text{ noncritical section} \\ \ell_2 \text{ } readyA := true \\ \ell_3 \text{ } turn := B \\ \ell_4 \text{ } \mathbf{await} (readyB = false \vee turn = A) \\ \ell_5 \text{ critical section} \\ \ell_6 \text{ } readyA := false \end{array} \right.$$

## Process B

repeat forever

$$\left( \begin{array}{l} m_1 \text{ noncritical section} \\ m_2 \text{ } readyB := true \\ m_3 \text{ } turn := A \\ m_4 \text{ } \mathbf{await} (readyA = false \vee turn = B) \\ m_5 \text{ critical section} \\ m_6 \text{ } readyB := false \end{array} \right.$$

1. What would be wrong with this protocol if we omitted the variable *turn*?
2. Express **Process A** as a CCS, CSP or ACP expression, featuring six atomic actions  $l_1, \dots, l_6$  and a recursive equation with variable *X*.
3. Represent **Process A** as a process graph, by using  $l_1, \dots, l_6$  as transition labels. Also attach names to the states by calling each state after the transition that is enabled there. Thus transition  $l_3$  goes from state  $l_3$  to state  $l_4$ .
4. Correct the answers to questions 2 and 3 by replacing the action  $l_4$  by the two actions “*B not ready*” and “*turn = A*”. “*B not ready*” denotes the action of reading the value of *readyB* and finding that it is *false*. Likewise, “*turn = A*” denotes the action of reading the value of *turn* and finding that it is A. [You may skip the original answers to 2 and 3.]

It would be possible to model instruction  $l_4$  assuming *busy waiting*, by drawing self-loops in state  $l_4$  labelled “*B ready*” and “*turn = B*”. However, I want to abstract from these unsuccessful read actions from the onset by not including them in our formal specification. Thus, the intuition of the **await** statement is that the process A patiently sits in state  $l_4$  until one of the transitions “*B not ready*” or “*turn = A*” is enabled.

5. Represent Peterson’s algorithm as Petri net with 18 places: 6 places for each of the 6 internal states of the processes A and B and 2 places for each of the 3 variables. You need 16 transitions: 2 copies of  $l_3$  and  $m_3$ , and 1 copy of each of the other transitions.

6. Represent the behaviour of variable *readyA* as a process graph. Its transition labels are the write actions  $l_2$  and  $l_6$  that can be performed by processes **A**, and the read action “*A not ready*” that can be performed by process *B*.
7. Give a process algebraic expression of this behaviour.
8. Represent the behaviour of variable *turn* as a process graph. Its transition labels are the write actions  $l_3$  and  $m_3$  that can be performed by processes **A** and **B**, and the read actions  $turn = A$  and  $turn = B$ . Also give a process algebraic expression of this behaviour.
9. Now give a process algebraic expression of the entire protocol, involving the parallel composition of 5 processes. All actions except the critical and noncritical sections ( $\ell_1$ ,  $\ell_5$ ,  $m_1$  and  $m_5$ ) are internal (only needed to make the protocol work) and should be abstracted away. (You may choose whether to use CCS, CSP or ACP, and feel free to rename for instance  $l_2$  into  $\overline{l_2}$  if this suits you.)
10. On the next page you see the potential states of a process graph representation of the entire algorithm. A state of the algorithm is completely determined by a state of process *A*, a state of process *B* and a state of *turn*. For the states of *readyA* and *readyB* are completely determined by the states of *A* and *B*. This observation yields  $6 \times 6 \times 2 = 72$  potential states.

Complete the given drawing into a process graph by supplying the transitions. Don't bother labelling them. Also don't draw loops backwards to the left or top rows; instead use the gray shadows, which represent copies of the transitions at the opposite end of the diagram. How many states are reachable from the initial state?

11. In this question we have an atomic proposition **IntentA**, saying that Process A intends to enter the critical section, but has not done so yet. It holds whenever Process A is in states  $\ell_2$ ,  $\ell_3$ ,  $\ell_4$  or  $\ell_5$ . We also have an atomic proposition **CritA**, saying that Process A is about to enter its critical section; it holds in state  $\ell_5$  only. Likewise we have atomic propositions **IntentB** and **CritB**.

Formulate an LTL property in terms of these four atomic propositions expressing the correctness of Peterson's algorithm. (See the beginning of this homework for the informal description of the two properties the protocol should have in order to be correct.)

# Process Graph of Peterson's Mutual Exclusion Algorithm

